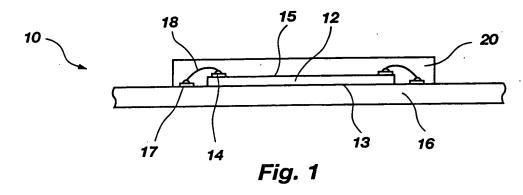
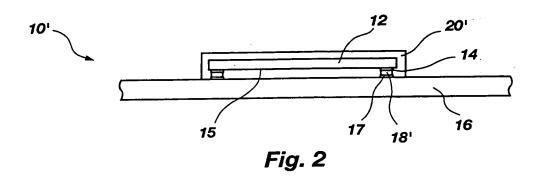


STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMSTIC SEMICONDUCTOR DEVICE PACKAGES AND STECONDUCTOR DEVICES INCLUDING STICOLITHOC. APHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US

6593171







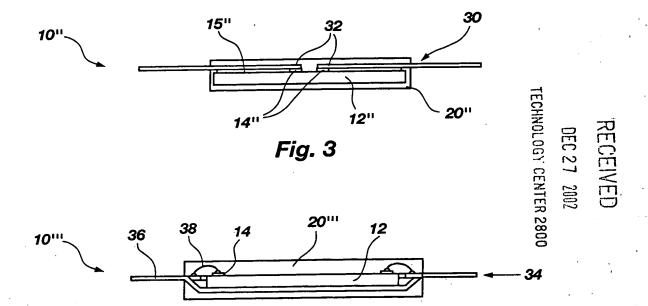
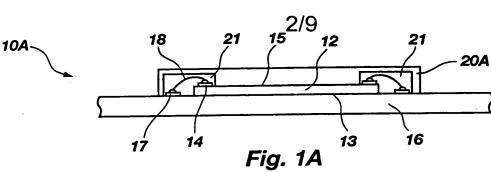
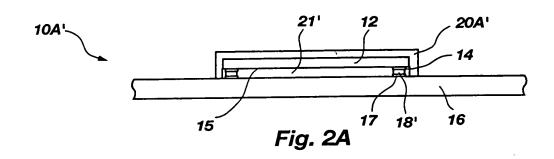


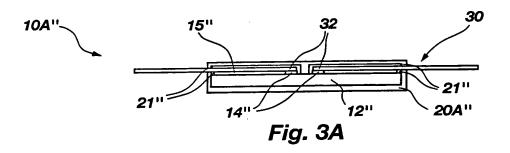
Fig. 4

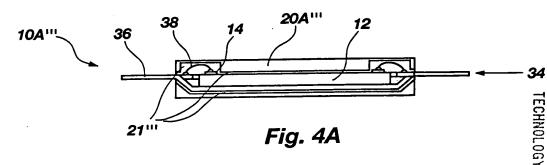


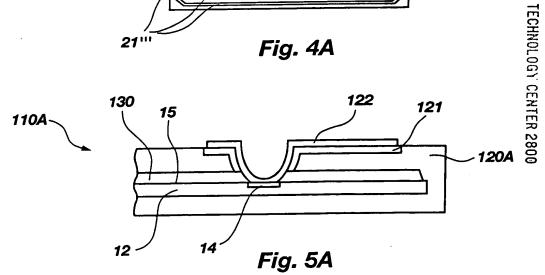
### STEREOLITHOGRAPHIC METHODS FOR FABRICATING HER METIC SEMICONDUCTOR DEVICE PACKAGES AND MICONDUCTOR DEVICES INCLUDING TREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US





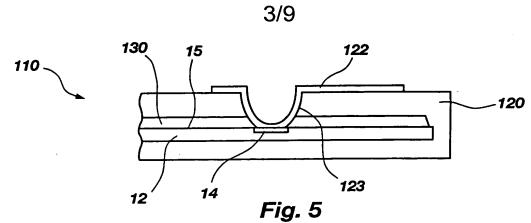








## STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STERI LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US



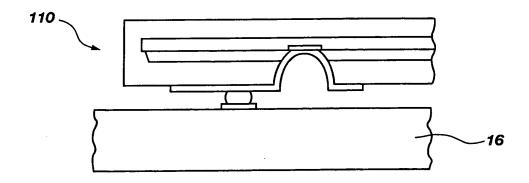


Fig. 6

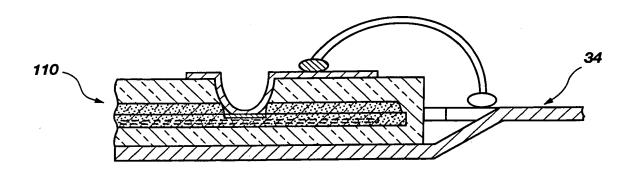


Fig. 7



## STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERM-TIC SEMICONDUCTOR DEVICE PACKAGES AND SEL ONDUCTOR DEVICES INCLUDING STER LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US

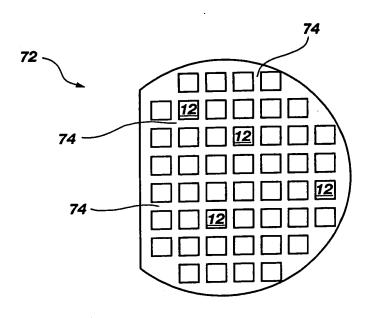


Fig. 8

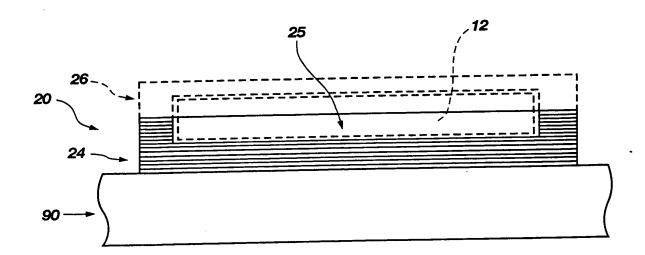
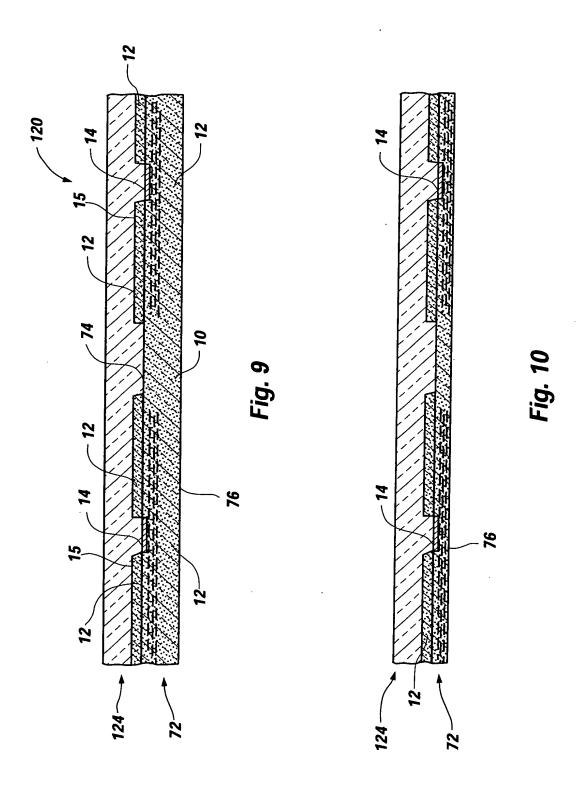


Fig. 18

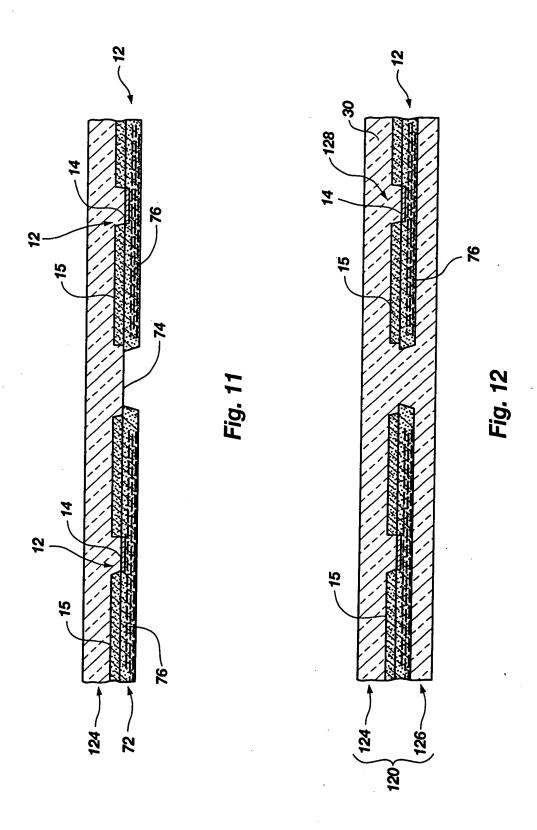


STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEM ONDUCTOR DEVICES INCLUDING STER LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US



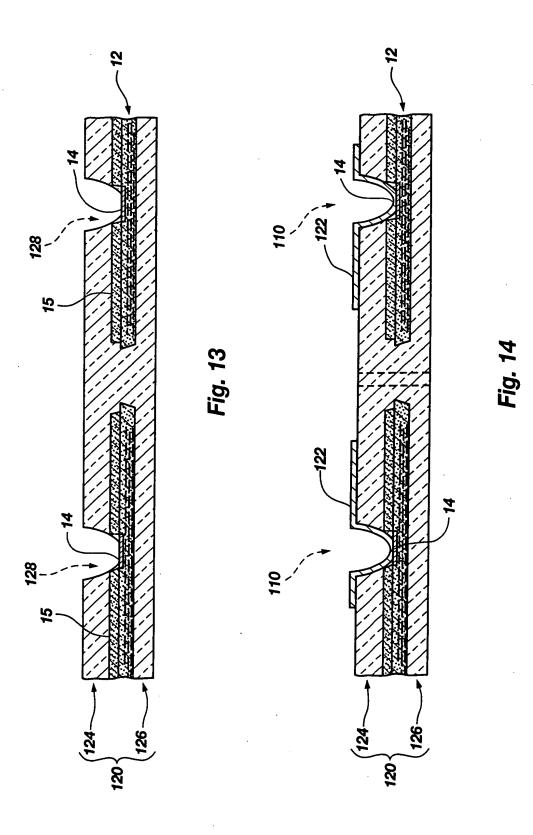


STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HERMANIC SEMICONDUCTOR DEVICE PACKAGE AND
SEM DONDUCTOR DEVICES INCLUDING STER
LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES
Inventor: Warren M. Farnworth
Serial No.: 10/074,840
Docket No.: 4212.3US





#### STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMSTIC SEMICONDUCTOR DEVICE PACKAGES AND SHOUNDUCTOR DEVICE PACKAGE AND SHOUNDUCTOR DEVICES INCLUDING STEED-LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US



PADE

STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HER STIC SEMICONDUCTOR DEVICE PACKAGES AND
SICCONDUCTOR DEVICES INCLUDING STICOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES
Inventor: Warren M. Farnworth
Serial No.: 10/074,840
Docket No.: 4212.3US

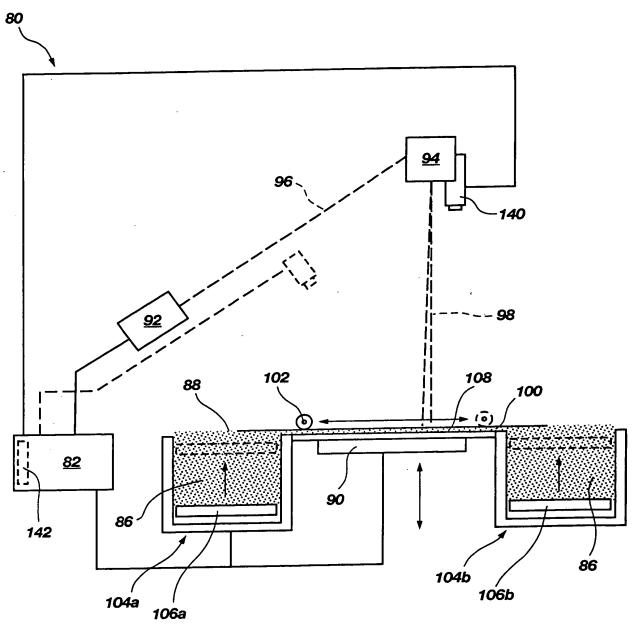


Fig. 15



# STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERM C SEMICONDUCTOR DEVICE PACKAGE AND SEL ONDUCTOR DEVICES INCLUDING STEL LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES Inventor: Warren M. Farnworth Serial No.: 10/074,840 Docket No.: 4212.3US

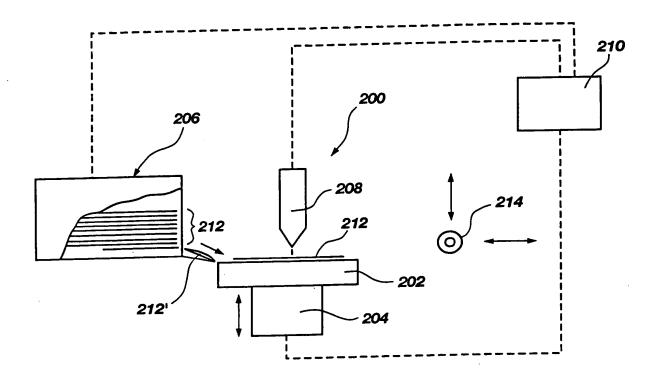


Fig. 16

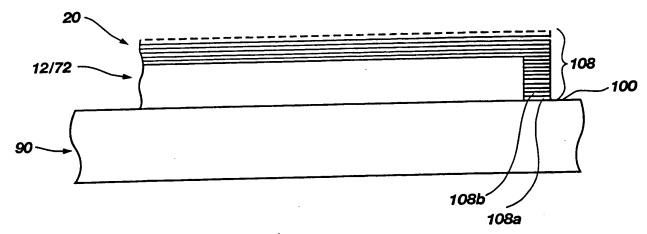


Fig. 17